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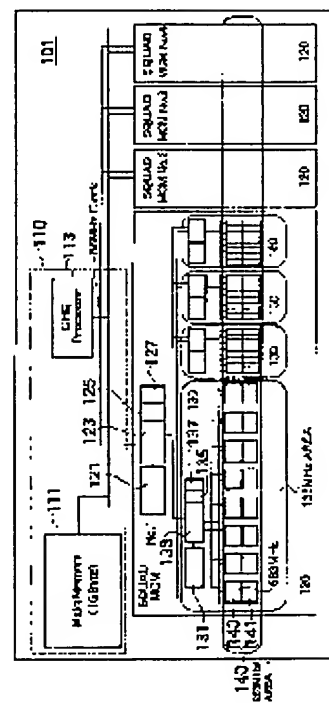
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## (54) PARALLEL COMPUTER HAVING HIERARCHICAL STRUCTURE

(57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a shared bus type parallel computer by which a desired number of high speed processors based on newest technique are operated in parallel.

**SOLUTION:** The processing unit of an intermediate hierarchy in a hierarchical shared bus multi-processor system is provided with a processor having a programmable ability being equal to that of a normal processor, an instruction memory and a data memory. Then a status signal from a low-order processor is received and a DMA controller having a large capacity of transfer data memory executes the compression and development of transfer data, a programmable load distribution and the load distribution in accordance with the working situation of the low-order processor.



## LEGAL STATUS

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